

Control of transmission power in a radio system

Field of the invention

The invention relates generally to a radio system. More specifically, the invention relates to automatic transmission power control in a radio system. Primary applications of the invention are in fixed radio links.

Background of the invention

In constructing fixed digital radio link connections, it is essential that the probability of the connection being broken remains sufficiently low. In addition to equipment reliability, this probability is dependent on phenomena in the radio path affecting the radio wave propagation. Such phenomena include:

1. normal free-space attenuation,
2. attenuation caused by rain, slowly changing in frequency and time,
3. splitting of the signal propagation path, due to changes in the atmospheric refractive index or reflections caused by terrain obstructions, into two or more routes (multipath propagation), and frequency-selective attenuation caused by the interaction of said routes, and
4. interference arriving at the receiver from other radio link hops or other equipment.

Consequently, the transmission power of the transmitter should be temporarily increased so that additional signal attenuation on account of phenomena 2 and 3 alone or in combination are not capable of excessively degrading the quality of the signal received. However, high transmission power increases, on the other hand, interference in adjacent channels and radio hops. This effect from phenomenon 4 of the increased interference brings about deterioration of the sensitivity of other receivers. Furthermore, high transmission power hinders the implementation of dense radio link networks and efficient utilization of the frequencies available for radio link systems.

The task of an automatic power control in a radio link system is to continuously adjust the transmission power of the transmitter according to the changes affecting the quality of the signal received so that error-free reception can be ensured as well as possible.

The power control mechanism has conventionally been implemented by making a signal level measurement on the received signal at the far end of

the radio hop. Then a signal representing the measured signal level is sent through a return channel to the transmitter and the transmission power is controlled so that the level of the received signal remains substantially constant at the receiving end.

- 5 A major drawback of this kind of power control system is that it is not capable of taking into account the effects of external interferences (phenomena 3 and 4) on the signal quality.

10 Some other known power control mechanisms are based on bit error measurement at the receiving end of the radio hop. In response to this measurement, the transmission power at the opposite end is then controlled in such a way that a predetermined error threshold is not exceeded at the receiving end. In this way, the effect of external interference can be taken into account in the power control, at least to some extent.

15 In addition to bit error measurement, the power control mechanism can also be based on other measurements made at the receiving end. A more sophisticated power control mechanism such as this is described in EP-B1-0428099. In this power control mechanism, the bit error rate and the rate of change of the received signal level are estimated by the receiver. If either the error rate estimate or, alternatively, the rate of change in the received signal level exceeds a respective predetermined threshold value, the transmission power is temporarily increased to a value appreciably higher than the normal transmission power but lower than the maximum transmission power. On the other hand, should both the error rate estimate and the rate of change in the received signal level simultaneously exceed the respective predetermined threshold value, the transmission power is increased to its maximum value for a predetermined time. This is done independently of the level of the received signal. After the predetermined time has elapsed, the transmitter power is gradually decreased until a certain signal level is reached, until bit errors appear again or until the rate of change of the received signal level exceeds the respective threshold value.

30 The drawback relating to the above known power control mechanisms based on bit error monitoring is that bit errors degrade the signal quality before the transmitter power can be increased. Furthermore, in systems such as the one described in EP-B1-0428099, the high or full transmission power may often be more than what is actually needed for error-free reception.

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Thus unnecessary interference may be generated for adjacent radio channels or systems.

Summary of the invention

5 The purpose of the present invention is to eliminate the drawbacks described above and to provide a power control mechanism which is capable of taking into account all the above phenomena affecting the performance of radio connections and which can also respond to degrading signal quality before bit errors begin to occur.

10 This objective is achieved with the solution defined in the independent patent claims.

 The invention is based on the detection of events called pseudo errors. In this context, "pseudo error" refers to a decision-making instant when a bit or symbol error nearly occurred, i.e. to instants when the right decision was actually made but the margin for the right decision was smaller than a
15 certain limit value so that an actual error was a close thing. The idea of the invention is to detect these pseudo errors at the receiving end and to decrease and maintain the transmission power at as low a level as possible so that pseudo errors occur only at rather long intervals. This is accomplished by
20 lowering the transmission power from a higher initial value until pseudo errors begin to occur and then maintaining the power at a correct level by increasing the power by a small amount when pseudo errors are detected and decreasing the power if the rate of the pseudo errors remains below a predetermined threshold.

25 With the solution in accordance with the invention, the transmission power can under all conditions be adjusted to a level which is just above the level where actual errors start to appear, irrespective of the actual signal level received (unless the signal level is included as a simultaneous adjustment criterion). Thus, the system is able to respond before any bit errors reach the
30 destination. Furthermore, no periods of high transmission power are needed which could cause unnecessary interference for the adjacent systems or channels.

 A further advantage of the invention is that a power control system can be introduced at low cost to the existing radio link terminals. This is
35 explained in more detail below.

Brief description of the drawings

In the following, the invention and its preferred embodiments are described in closer detail by way of example with reference to the accompanying drawings, wherein

5 Figure 1 shows a radio link system in accordance with the invention on a general level,

Figure 2 is a flow chart illustrating one embodiment of the power control method in accordance with the present invention,

10 Figure 3 is a flow chart illustrating another embodiment of the power control method in accordance with the present invention,

Figure 4 shows one embodiment of a link terminal receiver that can monitor pseudo errors,

15 Figure 5 illustrates a link terminal transmitter corresponding to the receiver of Figure 4,

Figure 6 shows an alternative embodiment of a link terminal receiver that can monitor pseudo errors, and

Figure 7 illustrates the detection of pseudo errors in the receiver of Figure 6.

Detailed description of the invention

20 In the following, the invention is described using as an example a radio link system constituting a bidirectional point-to-point connection. Figure 1 illustrates a digital radio link system of this type on a general level. The system comprises two radio link terminals A and B, communicating with one another
25 via a radio path RP. The figure uses like reference numerals for like parts, so that for radio link terminal A the reference numeral includes the letter a and for radio link terminal B the letter b. The transmitter (11a and 11b) and receiver (12a and 12b) of each link terminal are connected through a duplex filter, or alternatively through a switch (13a and 13b), to an antenna system (14a and
30 14b). Duplex filters are used when a bidirectional connection is desired, so that traffic passes simultaneously in both directions. The task of duplex filters is to separate the signals of the transmitter and receiver from each other. Duplex filters can be replaced with switches in a link system utilizing time division duplex communication.

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Both link terminals further comprise a control unit (15a and 15b, respectively) for controlling the link terminals and for performing measurements involved in the system, which may in practice involve several different tasks. However, from the point of view of the invention, the essential functionality of each control unit is that it controls the transmission power of the transmitter at the same end of the radio hop and sends a power control message to the opposite end of the radio hop.

Both receivers further include signal monitoring means (SMA and SMb) for monitoring the quality of the received signal. According to the invention, these signal monitoring means monitor at least the pseudo error occurrence in the receiver. In the preferred embodiments of the invention, further power adjustment criteria are used in addition to the pseudo error occurrence. Therefore, in these embodiments signal PE from the receiver may also include other measurement information besides data relating to the occurrence of pseudo errors. These preferred embodiments are described below after the control algorithm based on pseudo error occurrence has been described. The signal monitoring means or a part thereof can also be located in the control unit.

In the transmission direction from link terminal A to link terminal B, power control occurs as follows. Control unit 15b receives a pseudo error signal PE from the signal monitoring means in receiver 12b, i.e. a signal indicating whether pseudo errors have been detected in receiver 12b. On the basis of the contents of this signal, control unit 15b applies a power control message PCb to transmitter 11b which sends the said message through a return channel to the opposite end of the radio hop. Control unit 15a at the opposite end receives this message and in response controls the transmission power of transmitter 11a by supplying a control signal $c(t)$ to the transmitter. In the opposite transmission direction power control is performed in the same way by the corresponding unit at the opposite end. In other words, letter a should be changed to letter b, and vice versa, in the reference numerals of the above description.

The power control mechanism according to the present invention operates as follows.

1. Pseudo errors are monitored essentially continuously at the receiving end of the radio hop.

2. The transmission power of the transmitter is gradually decreased from its initial value which was set high enough so that no pseudo errors are detected at the receiving end, until the first pseudo error occurs. The transmission power is preferably decreased in small steps, consuming a sufficient time period at each step.

3. The transmitting end increases its transmission power by a small predetermined amount when one or more pseudo errors are detected. The transmission power is again decreased later, if no pseudo errors are detected within a time interval of predetermined length.

Figure 2 is a flow chart illustrating one embodiment of the power control method in accordance with the present invention. The figure illustrates steps of the method to be performed in addition to the monitoring of the pseudo errors, i.e. pseudo error monitoring is a background process for the steps shown in Figure 2. If it is detected (phase 20) by the receiver that a pseudo error has occurred, the transmission power of the transmitter (at the opposite end) is increased immediately by a predetermined small amount ΔP_1 , such as 1 or 2 dB (phase 21). After this, the system waits for a certain short delay period before it resets the variables used in connection with the pseudo error monitoring and starts the monitoring again (phase 21). The purpose of the delay is to ensure that the transmission power has reached its new value before the pseudo error monitoring is started again.

If no pseudo errors are detected, the system examines whether a sufficient time period (T1) has elapsed since the latest pseudo error (phase 22). If this is not the case, the system continues to search for the occurrence of pseudo errors. The purpose of this test is to prevent the transmission power from being decreased too soon after pseudo error is detected, i.e. a sufficient quality of the signal must first be ensured before the power level can be lowered again. On the other hand, if in phase 22 a sufficient time period is proved to have elapsed since the last pseudo error, the system also examines whether a sufficient time period (T2) has elapsed since the latest transmission power drop (phase 23). The purpose of this test is to prevent the power drops from occurring at too close intervals and to make sure that there is a sufficient time duration at each power level before the transmission power is lowered further. When this time interval has elapsed without any pseudo errors having

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and a FEC decoder in the receiver. FEC is a known error control method by means of which the receiving device is able to detect and correct any character or code block containing fewer than a predetermined number of erroneous symbols. FEC is accomplished by adding redundancy to each transmitted character or code block according to a predetermined algorithm. According to one feature of the FEC decoder, it can indicate if it has corrected one or more errors. This feature is utilized in the present invention so that these errors appearing at the decoder input are interpreted as pseudo errors, since they will be corrected in the decoder (assuming that the maximum performance of the decoder is not exceeded).

Figure 4 illustrates a receiver utilizing a FEC coder for pseudo error monitoring, and Figure 5 illustrates a transmitter sending the coded bit stream to the receiver of Figure 4. In the receiver, the received signal is supplied through the front end of the receiver (not shown in the figure) to a quadrature mixer 41 in which the signal is converted in a known manner by means of the signal of a local oscillator LO to baseband. If intermediate frequency is used in the reception, the signal SR supplied to the mixer is an IF signal, or an RF signal is supplied if direct conversion to the baseband is used. Quadrature baseband signals I and Q output from the mixer are amplified and filtered in an amplifier stage 42 before being supplied to a decision-making unit 43. The decision-making unit determines which symbols are being received. The symbol stream is then decoded in a symbol decoder 44, whereby the original FEC coded bit stream DATA_F is received from the decoder output. This bit stream is then supplied to a FEC decoder 45, which performs forward error decoding by removing, in a known manner, the redundancy which is added to the baseband bit stream in the FEC coder 51 in the transmitter (Figure 5). The FEC decoder has two outputs: the first output is for the corrected bit stream DATA and the second output is for the error signal (PE) indicating the corrections that the decoder has made. According to the invention, the corrections made by the decoder are interpreted as pseudo errors by the control unit, which then adjusts the transmission power of the transmitter according to the above-described algorithm.

In the transmitter (Figure 5), a FEC coder 50 is used as mentioned above. This coder is the last baseband signal processing unit in the transmission direction before modulator-related functions, i.e. it encodes the

baseband bit stream, which is otherwise ready for transmission. The FEC coded bit stream is then supplied through a symbol encoder 51, an I/Q modulator 52, RF head 53, and the duplex filter (or switch) 13 to antenna 14.

5 The receiver and the transmitter of the above first embodiment can be implemented in many ways. The only essential factor for the invention is that the FEC is used for monitoring pseudo errors by utilizing the FEC decoder feature which determines whether there were errors at its input even though its output is error-free.

10 According to the second embodiment of the invention, pseudo errors are detected by using extra decision thresholds in the demodulator. Figure 6 illustrates a receiver utilizing a demodulator in this manner. In Figure 6, the same reference numbers are used as in Figure 4 for the same parts. As can be seen in Figure 6, the information about pseudo errors is now received from the decision-making unit 63 provided with extra thresholds. In other respects
15 the receiver is the same as in the first embodiment, except that no FEC decoder is needed.

Figure 7 illustrates the detection of pseudo errors in the decision making unit 63, assuming that QPSK modulation is used in the transmission. The extra thresholds (i.e. pseudo error thresholds) are shown as dashed lines.
20 The dots in the figure represent signal values at the decision moments; the white dots represent symbols which are regarded as pseudo errors, and the black dots represent symbols not regarded as pseudo errors. As can be seen from the figure, the thresholds are set so that if the signal value is close to any of the actual symbol decision thresholds at the instant of symbol decision, the
25 event is regarded as a pseudo error.

In the above second embodiment, it is possible to obtain pseudo error data for every symbol, whereas in the above first embodiment, utilizing forward error correction, this data is received for every code block, i.e. less frequently than in the second embodiment. However, in practice this difference
30 is of no significance, as in both cases the error data is received quickly as compared to the finite speed of power control. In other words, in practice the power control is so "slow" that pseudo error data can be received at an appropriate rate in both embodiments. It is also to be noted that the extra thresholds in the demodulator operate correctly if the error in the symbol is not
35 so large that the symbol is clearly detected as one of the other symbols.

As mentioned above, in addition to pseudo errors other power adjustment criteria can be used in the power control. For example, signal monitoring means located in the receiver or in the control unit can measure the bit error rate continuously, in addition to monitoring the pseudo errors. In this way the algorithm based on pseudo errors can be overridden if the maximum performance of the pseudo error based algorithm is reached. In other words, if the error correction limits of the FEC decoder, for example, are reached so that too many errors begin to pass through the FEC decoder, the power control algorithm based on pseudo errors is temporarily overridden and the transmission power is immediately adjusted to its maximum value or to another high value. This additional measurement, which is used to override the pseudo error algorithm, can be any known measurement revealing the occurrence of actual errors.

There may also be a targeted received signal level set for the receiver as in prior art solutions. In that case, if this value is reached before the first pseudo errors occur, the reduction of the transmitter power ceases and, when necessary, the transmission power is increased.

The receiver can also monitor the rate of change of the received signal level as the receiver described in the above-mentioned EP-B1-0428099. In this case, if the received signal level is fluctuating faster than the power control system can respond, the transmit power can be increased to its maximum value until the fast signal level fluctuations disappear.

An additional advantage referred to only briefly in the above is that the power control mechanism can be implemented at low cost to existing receivers and transmitters, regardless of which of the above two implementation alternatives is chosen. In the first alternative, a known FEC mechanism can be utilized simply by monitoring the error output of the decoder. Moreover, the system may anyway use forward error coding for its primary purpose (for correcting errors), whereby the mechanism for pseudo error detection is obtained for free. In the second alternative, the extra thresholds can be implemented in the same ASIC circuit as the rest of the demodulator.

Even though the invention has been explained in the foregoing with reference to examples in accordance with the accompanying drawings, it is obvious that the invention is not restricted to these examples but can be

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modified within the scope of the inventive idea disclosed in the appended claims. The system need not necessarily be a conventional bidirectional point-to-point connection, but the same principle can be applied, for example, to unidirectional or point-to-multipoint radio connections with a suitable return

5 channel for sending the power control messages.

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